

**Amendments to the Specification:**

Please amend the paragraph, added in the preliminary amendment, above the "BACKGROUND OF THE INVENTION" on line 9 of page 1 as follows:

RELATED APPLICATION

This application is a divisional of U.S. Patent Application Serial No. 10/051,907, entitled "RAID 1 WRITE MIRRORING METHOD FOR HOST ADAPTERS," of B. Arlen Young filed on January 16, 2002, which issued as U.S. Patent No. ~~xx,xxx,xxx~~ 6,701,385 B1 on ~~XXXX,xx~~ March 2, 2004, and which is incorporated herein by reference in its entirety.

Please amend the paragraph, added in the preliminary amendment, after the paragraph ending on line 3 of page 22 as follows:

This application is related to the following divisional applications from the above cited parent application:

1. U.S. Patent Application Serial No. 10/~~xxx,xxx~~779,417, ~~(Attorney Docket No. ADPT105101)~~ entitled "A HARDWARE I/O CONTROL BLOCK STRUCTURE FOR MIRRORED AND NON-MIRRORED DATA TRANSFERS (As Amended)," of B. Arlen Young filed on February 12, 2004; and

2. U.S. Patent Application Serial No. 10/~~xxx,xxx~~779,416, ~~(Attorney Docket No. ADPT105103)~~ entitled "A METHOD FOR CONFIGURING A SINGLE HARDWARE I/O CONTROL BLOCK ARCHITECTURE FOR USE WITH BOTH MIRRORED AND NON-MIRRORED DATA TRANSFERS," of B. Arlen Young filed on February 12, 2004, each of which is incorporated herein by reference in its entirety.